



PATENT APPLICATION

042390.P5271

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

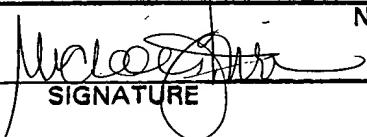
In re application of:) November 1, 1999
Rosenberg et al.)
Serial No.: 08/993,104) Group Art Unit: 2774
Filed: December 18, 1997) Examiner: F. Nguyen

For: VOLTAGE SIGNAL MODULATION SCHEME

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED
WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN
ENVELOPE ADDRESSED TO:
ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231, ON: NOVEMBER 2, 1999
Date of Deposit

INTEL CORPORATION

Name of Assignee


SIGNATURE11-2-99
DATE

RESPONSE

HONORABLE ASSISTANT COMMISSIONER FOR PATENTS,
Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed August 2, 1999, please reconsider
the above-identified patent application in view of the remarks and amendments
below. Applicants would like to thank the Examiner for his efforts in identifying
issues with this application.

Applicants believe the following is a complete response to the issues raised
by the Examiner and that the following amendments and comments place the
application into condition for allowance.

RECEIVED
NOV 10 1999
TC 2700 MAIL ROOM